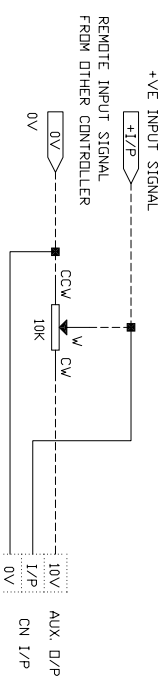
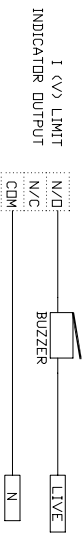


LOGIC LOW < 0.7Vdc  
LOGIC HIGH > 3.6V (OUTPUT DFP)

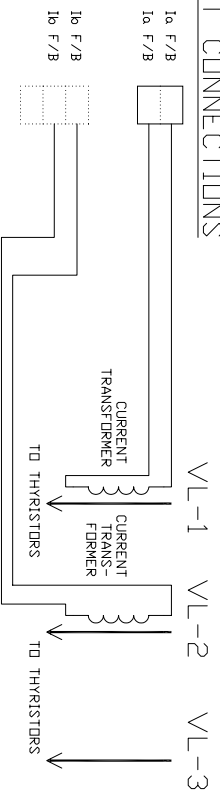
LOG INPUT



V OR I LIMIT INDICATOR D/P

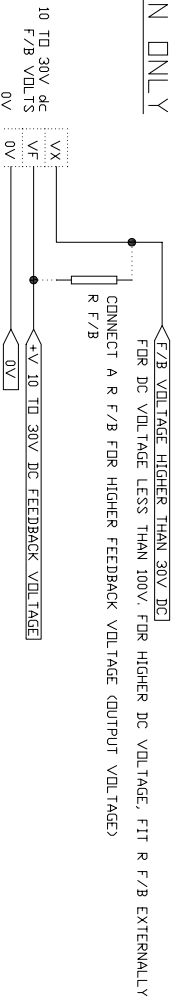


OPTIONAL CURRENT LIMIT CONNECTIONS



V F/B OPTION ONLY

FIG. 4



- o) FOR CONSTANT VOLTAGE CONTROL, WITH INPUT SIGNAL, SW3 AT ON AND SW4 AT OFF POSITION, ADJUST I/P SPAN POT TO GIVE MAX. OUTPUT CONTROL VOLTAGE. WITH INPUT AT MIN. LEVEL, ADJUST ZERO POT TO GIVE ZERO OUTPUT.
- b) FOR TESTING THE CONTROLLER WITHOUT FEEDBACK SIGNAL, PUT SW3 AT 'OFF' AND SW4 AT 'ON' POSITION, IT THEN SELECT THE CONTROLLER WORKING AT OPEN LOOP CONTROL. THE OUTPUT FOLLOWS THE INPUT SIGNAL.

POWERLINK ELECTRONICS LTD.

DESIGNED BY	DATE	CLIENT
DRAWN BY	DATE	TITLE
CHECKED BY	DATE	CONTROL SIGNAL CONNECTION DIA.
APPROVED BY	DATE	FOR PC340 / 350

PROJECT No:

REV A BATE 31 MAR-90  
REV B APR 1-90  
REV C MAR-91  
REV D JUNE -95

ISSUE

AMENDMENT

MADE BY

DATE

SCALE:

SHEET :

A3

DRAWING No:

CD6011D

R